



Keynote Address 1

Tuesday 24th May 2022, 9:30 am CET

Dr. Mateo Valero Cortés

Director, Barcelona Supercomputing Center, Spain

Supercomputers and European Sovereignty ...



Keynote Address 2

Wednesday 25th May 2022, 8:30 am CET

Dr. Yu Huang

EDA Chief Architect, HiSilicon Technologies Co. Ltd, China

AI-Assisted Yield Learning



Keynote Address 3

Wednesday 25th May 2022, 3:00 pm CET

Kaushik Narayanun

Vice-President Engineering, NVIDIA, USA

DFX: Exploring the Design Space for Quality



Keynote Address 4

Thursday 26th May 2022, 8:30 am CET

Dr.-Ing. Mehmet Kaynak

Leibniz Institute for High Performance Microelectronics (IHP), Germany

*SiGe BiCMOS Technology with Advanced Integration
Solutions for mm-Wave and THz Applications*